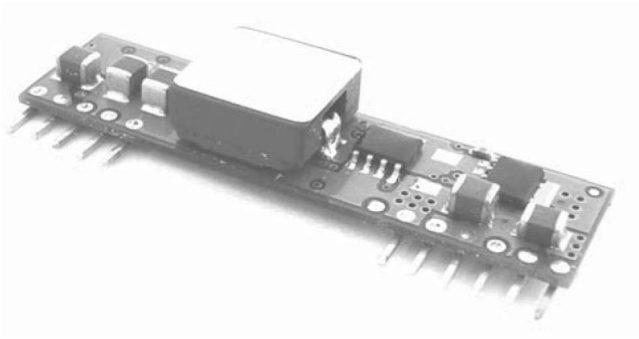
CUI INC
a bel group

SERIES: VPOL10A-12W-SIP | DESCRIPTION: POINT OF LOAD CONVERTER

FEATURES

- industry standard pin out
- high efficiency to 90%
- 300 KHz switching frequency
- 6.0-14 VDC wide input range
- 0.75-5.0 VDC wide output range
- over temperature protection
- continuous short circuit protection
- remote on/off
- cost-efficient open frame design
- UL/C-UL 60950-1 (E222736) certified
- output voltage sequencing (tracking)
- power good signal



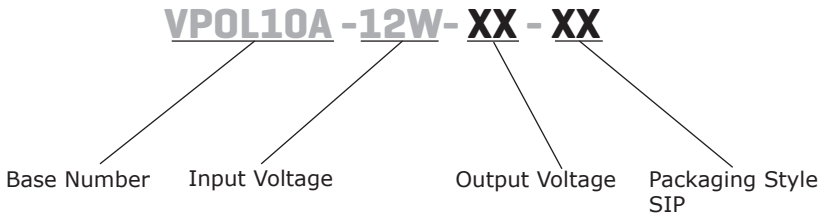
RoHS

MODEL

MODEL	input voltage typ (Vdc)	input voltage range (Vdc)	output voltage (Vdc)	output current max (A)	output power max (W)	ripple and noise[1] max (mVp-p)	efficiency typ (%)
VPOL10A-12W-SIP	12	6.0~14	0.7525	10	3.75	75	82
		6.0~14	1.2		6.0	75	87
		6.0~14	1.5		7.5	75	89
		6.0~14	1.8		9.0	75	90
		6.0~14	2.0		10.0	75	91
		6.0~14	2.5		12.5	75	92
		6.0~14	3.3		16.5	75	93
		6.5~14	5.0		25.0	75	95

Notes: 1. Ripple and noise are measured at 20 MHz BW by "parallel cable" method with 1 µF ceramic and 10 µF electrolytic capacitors on the output.

PART NUMBER KEY



INPUT

parameter	conditions/description	min	typ	max	units
operating input voltage		6.0	12	14.0	Vdc
input under-voltage lockout	turn-on voltage treshold turn-off voltage treshold lockout hysteresis voltage		5.0 4.0 1.0		Vdc Vdc Vdc
input current	0~14 Vdc			10	A
no load input current	0.7525 output 1.2 output 1.5 output 1.8 output 2.0 output 2.5 output 3.3 output 5.0 output		40 40 50 50 60 65 75 95		mA mA mA mA mA mA mA mA
off converter input current	shutdown input idle current			10	mA
inrush current				0.4	A^2s
input reflected-ripple current	P-P thru 1µH inductor, 5Hz to 20MHz		200		mA
leakage current				1	mA

OUTPUT

parameter	conditions/description		min	typ	max	units
capacitive load	low ESR				8,000	µF
output voltage set point	at nominal input			±1.5		%
trim adjustment range	selected by an external resistor		0.7525		5.0	Vdc
line regulation	low to high			±0.2		%
load regulation	10~100% load			±0.5		%
operating current range			0		10	A
switching frequency				300		kHz
start-up time				3		ms
rise time				4		ms
remote on/off	positive logic logic low (module off) logic high (module on)	or open circuit	0		0.4	Vdc
	negative logic logic low (module off) logic high (module on)	or open circuit	0 2.8		0.4	Vdc
temperature coefficient	-40~85°C			±0.03		%/°C

PROTECTIONS

parameter	conditions/description	min	typ	max	units
short circuit protection	continuous, auto recovery				
over temperature protection			130		°C

SAFETY AND COMPLIANCE

parameter	conditions/description	min	typ	max	units
safety approvals	60950-1: UL/cUL				
MTBF	as per MIL-HDBK-217F @ 25°C	920,000			hours
RoHS	yes				

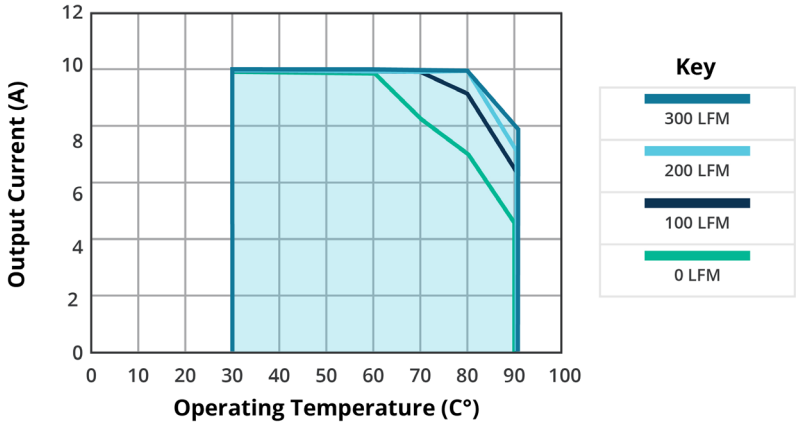
ENVIRONMENTAL

parameter	conditions/description	min	typ	max	units
operating temperature	see derating curve	-40		85	°C
storage temperature		-55		125	°C

DERATING CURVES

Figure 1

TEMPERATURE DERATING CURVE
(5 Vdc input & 3.3 Vdc output)



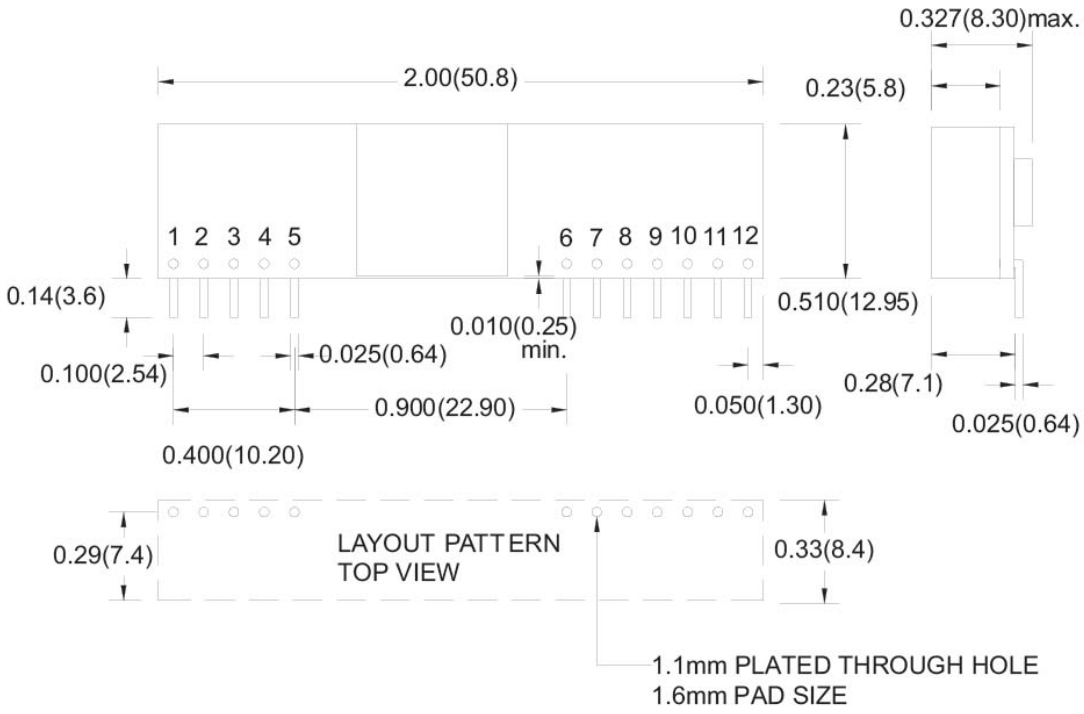
MECHANICAL

parameter	conditions/description	min	typ	max	units
dimensions	50.8 x 12.95 x 8.3 (2.0 x 0.51 x 0.327 inch)				mm
weight			8.5		g

MECHANICAL DRAWING

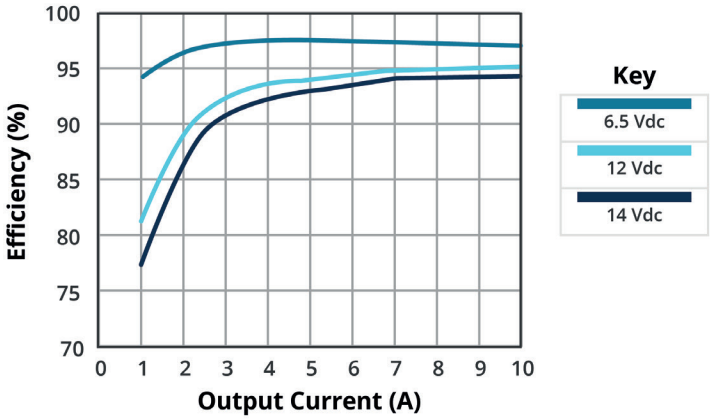
units: inches [mm]
tolerance: x.xx ±0.02 [±0.5], x.xxx ±0.010 [0.25]

PIN CONNECTIONS	
Pin	Function
1	+Vo
2	+Vo
3	+Sense
4	+Vo
5	Common
6	No pin / PGood
7	Common
8	+Vin
9	+Vin
10	Sequency
11	Trim
12	REM

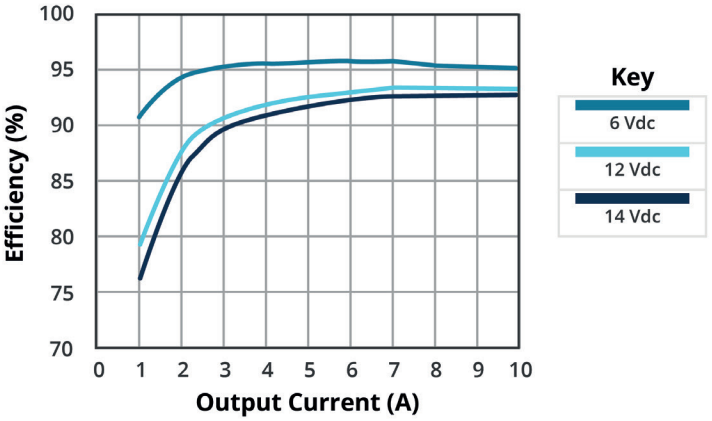


EFFICIENCY CURVES

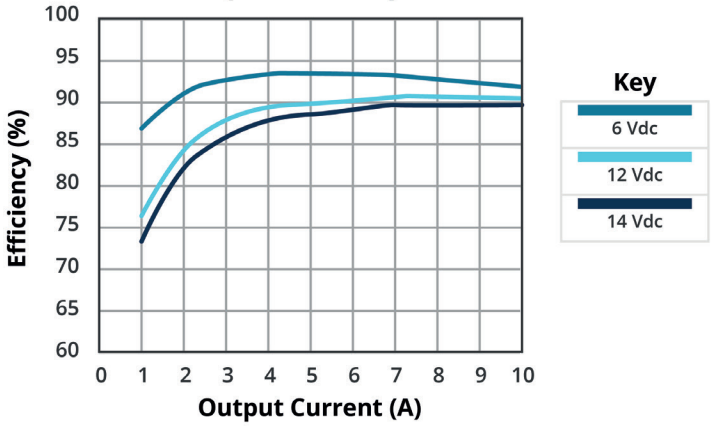
EFFICIENCY VS OUTPUT CURRENT
VPOL10A-12W-SIP
(Vo = 5.0 Vdc)



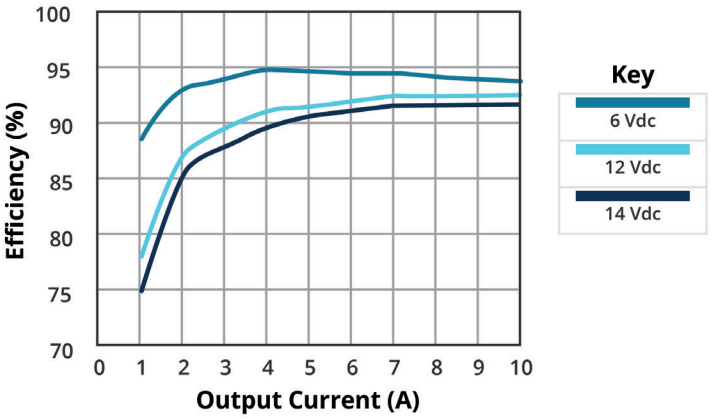
EFFICIENCY VS OUTPUT CURRENT
VPOL10A-12W-SIP
(Vo = 3.3 Vdc)

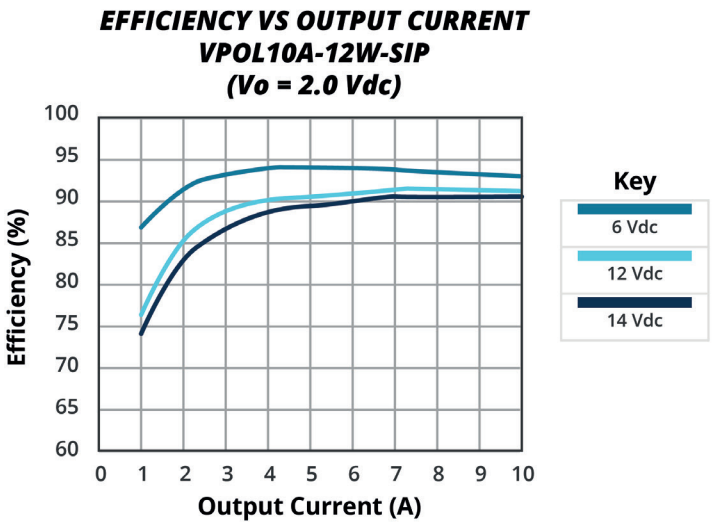


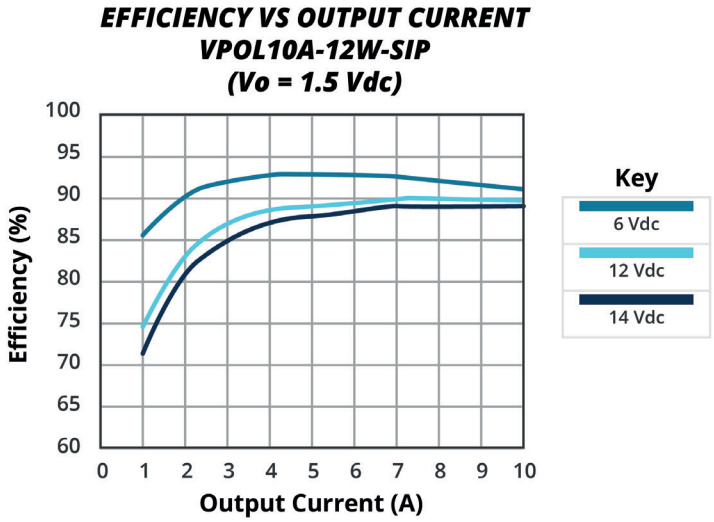
EFFICIENCY VS OUTPUT CURRENT
VPOL10A-12W-SIP
(Vo = 1.8 Vdc)

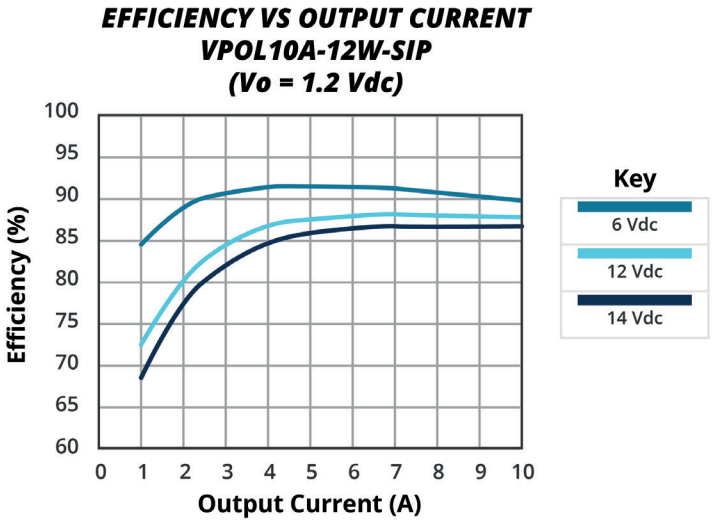


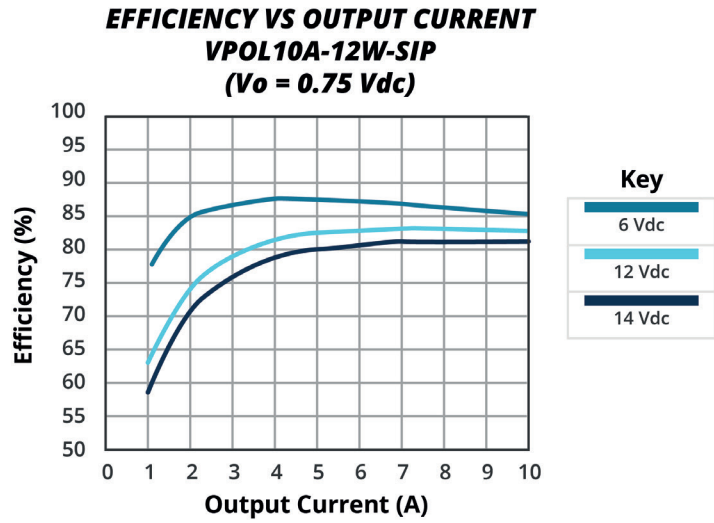
EFFICIENCY VS OUTPUT CURRENT
VPOL10A-12W-SIP
(Vo = 2.5 Vdc)




EFFICIENCY VS OUTPUT CURRENT
VPOL10A-12W-SIP
(Vo = 2.0 Vdc)
Efficiency (%)
100
95
90
85
80
75
70
65
60
0 1 2 3 4 5 6 7 8 9 10
Output Current (A)
Key
6 Vdc
12 Vdc
14 Vdc


EFFICIENCY VS OUTPUT CURRENT
VPOL10A-12W-SIP
(Vo = 1.5 Vdc)
Efficiency (%)
100
95
90
85
80
75
70
65
60
0 1 2 3 4 5 6 7 8 9 10
Output Current (A)
Key
6 Vdc
12 Vdc
14 Vdc

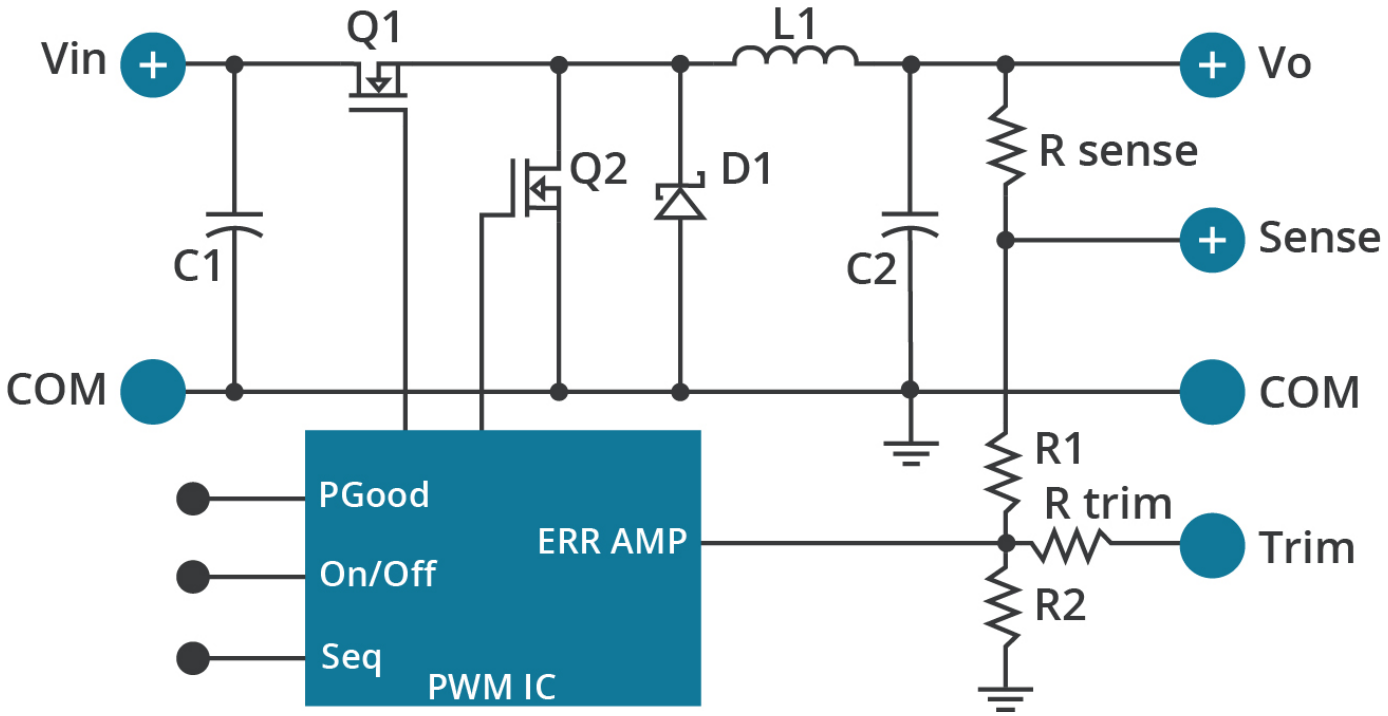

EFFICIENCY VS OUTPUT CURRENT
VPOL10A-12W-SIP
(Vo = 1.2 Vdc)
Efficiency (%)
100
95
90
85
80
75
70
65
60
0 1 2 3 4 5 6 7 8 9 10
Output Current (A)
Key
6 Vdc
12 Vdc
14 Vdc


EFFICIENCY VS OUTPUT CURRENT
VPOL10A-12W-SIP
(Vo = 0.75 Vdc)
Efficiency (%)
100
95
90
85
80
75
70
65
60
55
50
0 1 2 3 4 5 6 7 8 9 10
Output Current (A)
Key
6 Vdc
12 Vdc
14 Vdc

THEORY OF OPERATION

A block diagram of the VPOL10A-12W-SIP Series converter is shown in Figure 2. Extremely high efficiency power conversion is achieved through the use of synchronous rectification and drive techniques. Essentially, the powerful VPOL10A-12W-SIP series topology is based on a non-isolated synchronous buck converter. The control loop is optimized for unconditional stability, fast transient response and a very tight line and load regulation. In a typical pre-bias application the VPOL10A-12W-SIP series converters do not draw any reverse current at start-up. The output voltage can be adjusted from 0.7525 to 5.0Vdc, using the TRIM pin with a external resistor. The converter can be shut down via a remote ON/OFF input that is referenced to ground. This input is compatible with popular logic devices; a 'positive' logic input is supplied as standard. Positive logic implies that the converter is enabled if the remote ON/OFF input is high (or floating), and disabled if it is low. The converter is also protected against over-temperature conditions. If the converter is overloaded or the ambient temperature gets too high, the converter will shut down to protect the unit.

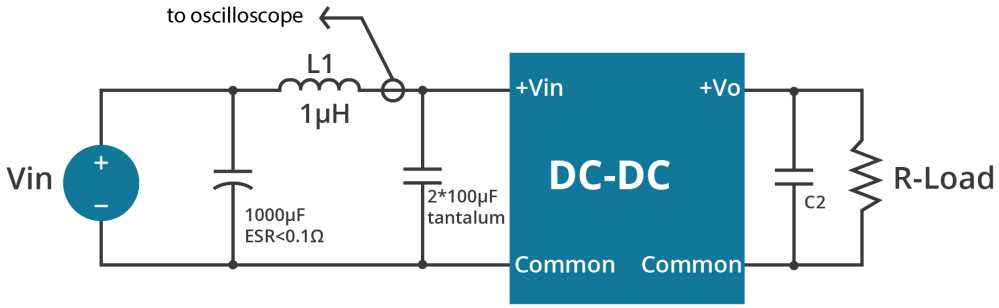
Figure 2



TYPICAL APPLICATION CIRCUITS

The VPOL10A-12W-SIP converters must be connected to a low AC source impedance. To avoid problems with loop stability source inductance should be low. Also, the input capacitors should be placed close to the converter input pins to de-couple distribution inductance. However, the external input capacitors are chosen for suitable ripple handling capability. Low ESR polymers are a good choice. They have high capacitance, high ripple rating and low ESR (typical <100mΩ). Electrolytic capacitors should be avoided. Circuit as shown in Figure 3 represents typical measurement methods for ripple current. Input reflected-ripple current is measured with a simulated source Inductance of 1µH. Current is measured at the input of the module.

Figure 3
Input Reflected-Ripple Test Setup



The basic test set-up to measure parameters such as efficiency and load regulation is shown in Figure 4. Things to note are that this converter is non-isolated, as such the input and output share a common ground. These grounds should be connected together via low impedance ground plane in the application circuit. When testing a converter on a bench set-up, ensure that -Vin and -Vo are connected together via a low impedance short to ensure proper efficiency and load regulation measurements are being made. When testing the VPOL10A-12W-SIP series under any transient conditions please ensure that the transient response of the source is sufficient to power the equipment under test. We can calculate the Efficiency, Load regulation and Line regulation.

The value of efficiency is defined as:

$$Efficiency = \frac{Vo \times Io}{Vin \times Iin} \times 100\%$$

Note: Vo is output voltage, Io is output current, Vin is input voltage, Iin is input current.

The value of load regulation is defined as:

$$Load.reg = \frac{V_{FL} - V_{NL}}{V_{NL}} \times 100\%$$

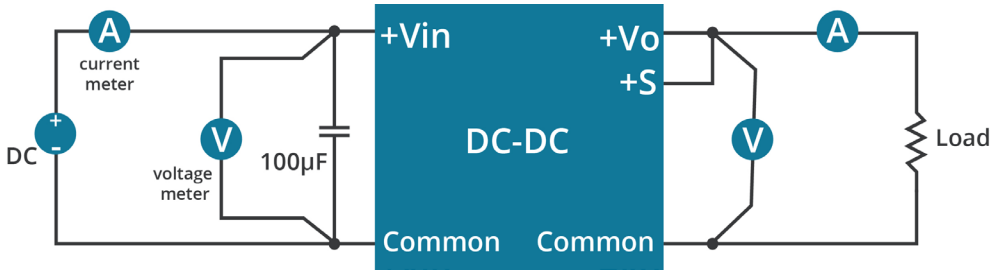
Note: V_{NL} is the output voltage at no load.

The value of line regulation is defined as:

$$Line.reg = \frac{V_{HL} - V_{LL}}{V_{LL}} \times 100\%$$

Note: V_{HL} is the output voltage of maximum input voltage at full load.
V_{LL} is the output voltage of minimum input voltage at full load.

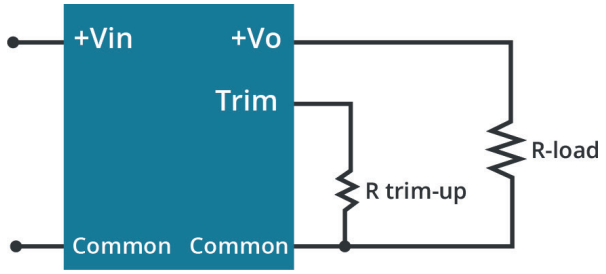
Figure 4
VPOL10A-12W-SIP Series Test Setup



TYPICAL APPLICATION CIRCUITS (CONTINUED)

The output voltage of the VPOL10A-12W-SIP can be adjusted in the range 0.7525V to 5.0V by connecting a single resistor on the motherboard (shown as Rtrim) in Figure 5. When Trim resistor is not connected the output voltage defaults to 0.7525V.

Figure 5
Trim-up Voltage Setup



The value of Rtrim-up defined as:

$$Rtrim = \left(\frac{10500}{Vo - 0.7525} - 1000\right)$$

Note: Rtrim-up is the external resistor in Ω, Vo is the desired output voltage

To give an example of the above calculation, to set a voltage of 3.3 Vdc.
Rtrim is given by:

$$Rtrim = \left(\frac{10500}{3.3 - 0.7525} - 1000\right)$$

Note: Rtrim = 3122 kΩ

For various output values various resistors are calculated and provided in Table 1 for convenience:

Table 1
Trim Resistor Values

Vo, set (V)	Rtrim (kΩ)
0.7525	open
1.2	22.46
1.5	13.05
1.8	9.024
2.0	7.417
2.5	5.009
3.3	3.122
5.0	1.472

TYPICAL APPLICATION CIRCUITS (CONTINUED)

Remote on/off

The remote ON/OFF input feature of the converter allows external circuitry to turn the converter ON or OFF. Active-high remote ON/OFF is available as standard. The VPOL10A-12W-SIP are turned on if the remote ON/OFF pin is high(=Vin), or left open. Setting the pin low(<0.4Vdc) will turn the converter 'Off'. The signal level of the remote on/off input is defined with respect to ground. If not using the remote on/off pin, leave the pin open (module will be on). The part number suffix "N" is Negative remote ON/OFF version. The unit is guaranteed OFF over the full temperature range if this voltage level exceeds 2.8Vdc. The converters are turned on If the on/off pin input is low (<0.4Vdc) or left open. The recommended VPOL10A-12W-SIP remote on/off drive circuit as shown in Figures 6 & 7.

Figure 6
Positive Remote on/off Input Drive Circuit

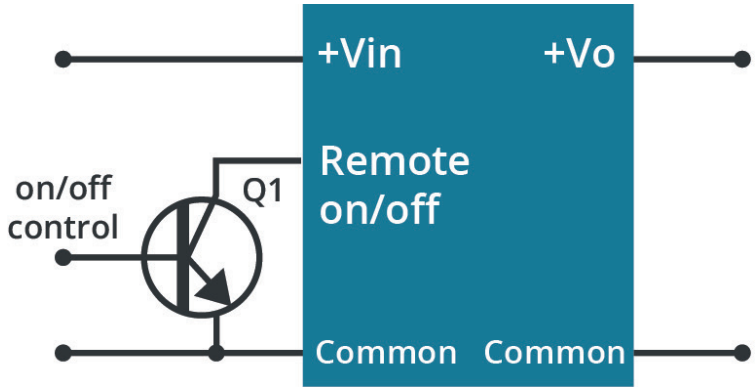
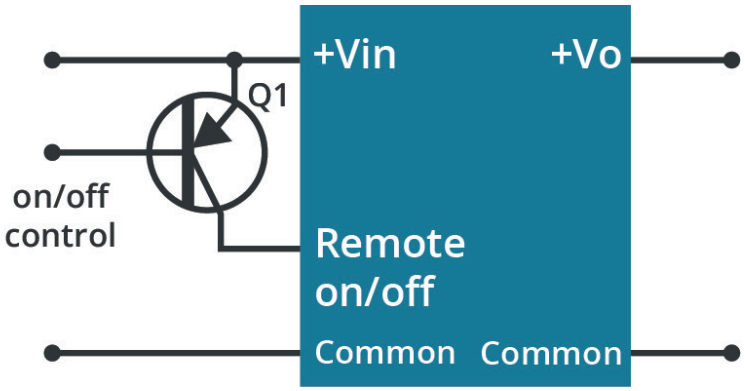


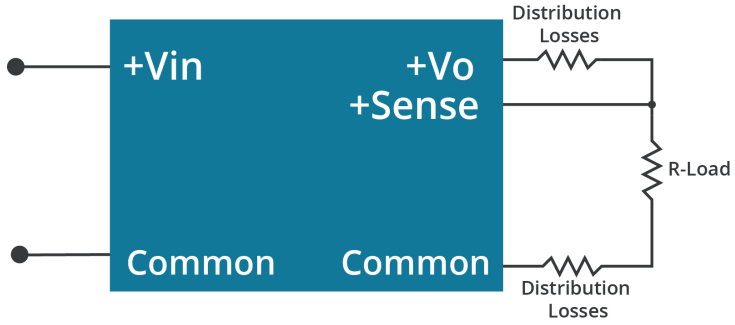
Figure 7
Negative Remote On/Off Input Drive Circuit



REMOTE SENSE COMPENSATION

Remote Sense regulates the output voltage at the point of load. It minimizes the effects of distribution losses such as drops across the connecting pin and PCB tracks (see Figure 8).
Please note however, the maximum drop from the output pin to the point of load should not exceed 500mV for remote compensation to work. The amount of power delivered by the module is defined as the output voltage multiplied by the output current (VO x IO). When using TRIM UP, the output voltage of the module will increase which, if the same output current is maintained, increases the power output by the module. Make sure that the maximum output power of the module remains at or below the maximum rated power. When the Remote Sense feature is not being used, leave sense pin disconnected.

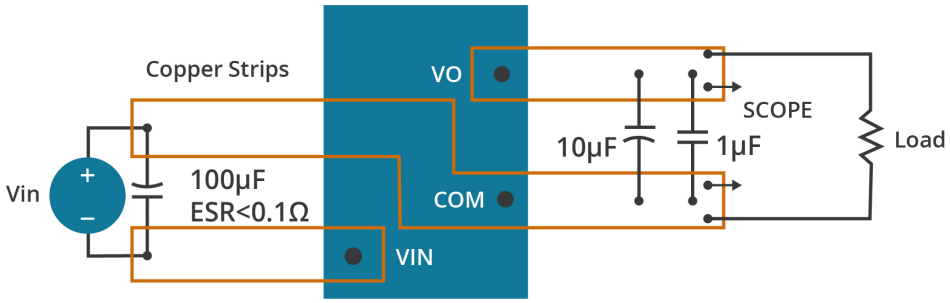
Figure 8
Circuit Configuration for Remote Sense Operation



OUTPUT RIPPLE AND NOISE MEASUREMENT

The test set-up for noise and ripple measurements is shown in Figure 9 a coaxial cable with a 50Ω termination was used to prevent impedance mismatch reflections disturbing the noise readings at higher frequencies. The Output Ripple & Noise is measured with 1µF ceramic and 10µF tantalum. The measured position are 50mm to 75mm (2" to 3") from the module.

Figure 9
Output Voltage Ripple and Noise Measurement Set-up



Output Capacitance

The VPOL10A-12W-SIP series converters provide unconditional stability with or without external capacitors. For good transient response low ESR output capacitors should be located close to the point of load. For high current applications point has already been made in layout considerations for low resistance and low inductance tracks. Output capacitors with its associated ESR values have an impact on loop stability and bandwidth. CUI INC's converters are designed to work with load capacitance up-to 8,000µF. It is recommended that any additional capacitance, typically 1,000µF and low ESR (<20mΩ), be connected close to the point of load and outside the remote compensation point.

REVISION HISTORY

rev.	description	date
1.0	initial release	08/01/2007
1.01	datasheet updated to a new template	11/02/2021

The revision history provided is for informational purposes only and is believed to be accurate.

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