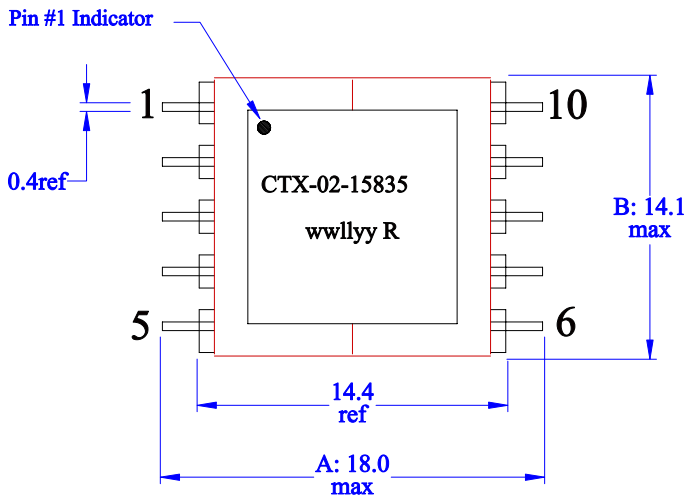
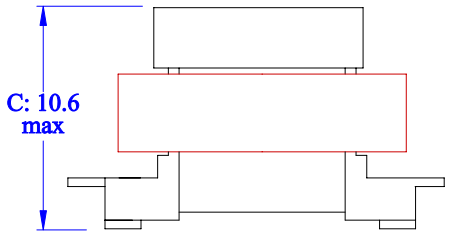
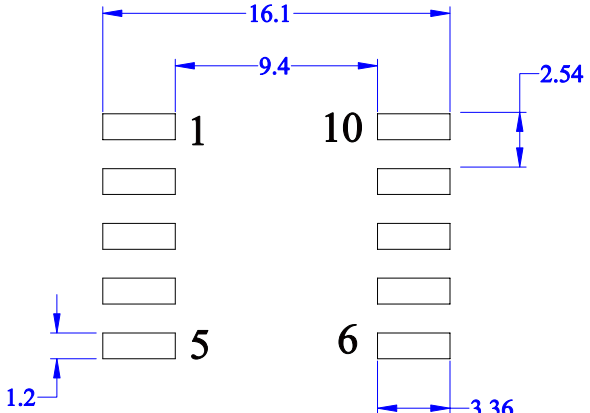
TOP VIEW



FRONT VIEW



RECOMMENDED PCB LAYOUT

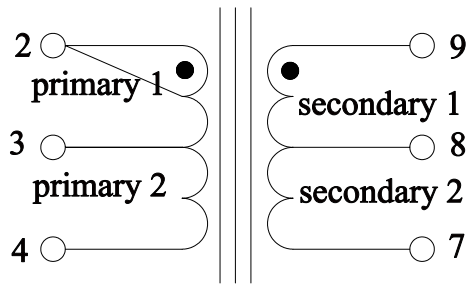


Component Side

ELECTRICAL CHARACTERISTICS

OCL @ 100KHz, 0.10Vrms, 0.0Adc; pins (2 - 3). 375 - 625uH.
DCR @ 20 degrees C., pins (2 - 3). 0.25 Ohm maximum.
DCR @ 20 degrees C., pins (3 - 4).: 0.25 Ohms maximum.
DCR @ 20 degrees C., pins (9 - 8). 0.52 Ohm maximum.
DCR @ 20 degrees C., pins (8 - 7).: 0.52 Ohms maximum.
Turns ratio: pins (2 - 3) : (3 - 4) : (9 - 8) : (8 - 7). 1.0:1.0:1.4:1.4
HiPot @ 1500Vdc for 1 second; pins 2 to 9.
HiPot @ 600Vdc for 1 second; pins 2 & 9 to core.
SCL @ 100KHz, 0.10Vrms, 0.0Adc; pins (2 - 4) with (9-7) shorted 5.0 uh max

SCHEMATIC



Notes:

1). All dimensions are in mm unless otherwise specified.
2). Tolerances are +/- 0.25 millimeters unless stated otherwise.
3). All soldering surfaces must be coplanar within 0.1524 millimeters.
4). Core wrapped with 2 layers of epoxy tape.
5). wwllyy= Date Code, R= revision level
6). PCB tolerances are +/-0.1 millimeters unless stated otherwise.

TEMPLATE NAME: SG7.dot REV X5

COILTRONICS, INCORORATED
3601 Quantum Blvd, Boynton Beach, FL 33426

Transformer, push-pull, continuous
4.5 - 5.5Vdc(input) : 5Vdc @ 0.60A

100kHz, 500uH @ 0.72Adc(peak)
EE12.6, 10 Pin J-Lead, Horizontal, SMT

Size	Drawing Number:	CTX02-15835	
A	Revision Level:	A	Sheet 3 of 6