SY100EP16V

3.3V/5V Precision ECL Differential Receiver/Driver

General Description

The SY100EP16V is a 3.3/5V, high-speed, fully differential ECL Receiver/Driver that can process clock signals as fast as 3GHz or data patterns up to 2.5Gbps.

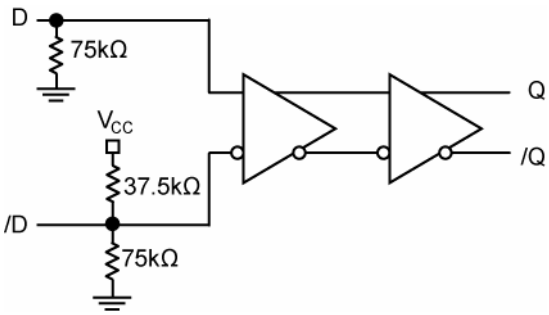
The differential input includes an internal 75kΩ pull-down resistor on both the true and complement inputs and a 37.5kΩ pull-up resistor in the complement input only. For AC-coupled input interface applications, an integrated voltage reference (V_{BB}) is provided to bias for either single-ended use or for DC biasing when AC coupling to the device. The V_{BB} pin should be used only as a bias for the SY100EP16V since its current sink/source capability is limited. Whenever used, the V_{BB} pin should be bypassed to the most positive supply voltage. The output is 800mV ECL, with fast rise/fall times guaranteed to be less than 200ps.

When the inputs are left open, the internal input clamps will force the Q output low.

The SY100EP16V operates from a 3.3V or 5V ±10% supply and is guaranteed over the full industrial temperature range (–40°C to +85°C).The SY100EP16V is part of Micrel's high-speed, Precision Edge® product line.

Datasheets and support documentation can be found on Micrel's web site at: www.micrel.com.

Functional Block Diagram



Precision Edge®

Features

- Precision 800mV ECL Receiver/Driver
- 100K ECL compatible I/O
- Guaranteed AC performance over temperature and voltage:
  - DC-to > 2.5Gbps throughput
  - 220ps typical propagation delay (D-to-Q)
  - <200ps rise/fall times
- PECL and NECL operation capability
- PECL operating range: V_{CC} = 3.0V to 5.5V with V_{EE} = 0V
- NECL operating range: V_{CC} = 0V with V_{EE} = -3.0V to -5.5V
- 3.3V ±10% or 5.0V ±10% power supply operation
- Industrial temperature range: –40°C to +85°C
- Available in 8-pin (2mm x 2mm) MLF®, 8-pin MSOP, and 8-pin SOIC packages

Applications

- All SONET clock and data distribution
- Fibre Channel clock and data distribution
- Gigabit Ethernet clock and data distribution
- Backplane distribution

Markets

- Storage
- ATE
- Test and measurement
- Enterprise networking equipment
- High-end servers
- Access
- Metro area network equipment

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MLF and *Micro*LeadFrame are registered trademarks of Amkor Technology.

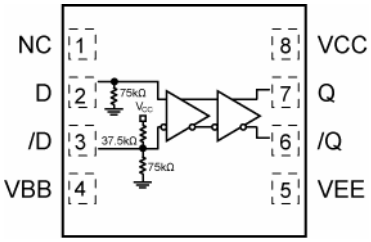
Ordering Information[1]

Part Number	Package Type	Operating Range	Package Marking	Lead Finish
SY100EP16VMG	MLF-8	Industrial	162 with Pb-Free bar-line indicator	NiPdAu Pb-Free
SY100EP16VMGTR[2]	MLF-8	Industrial	162 with Pb-Free bar-line indicator	NiPdAu Pb-Free
SY100EP16VZG	SOIC-8	Industrial	XEP16V with Pb-Free bar-line indicator	NiPdAu Pb-Free
SY100EP16VZGTR[2]	SOIC-8	Industrial	XEP16V with Pb-Free bar-line indicator	NiPdAu Pb-Free
SY100EP16VKG	MSOP-8	Industrial	XP16V with Pb-Free bar-line indicator	NiPdAu Pb-Free
SY100EP16VKGTR[2]	MSOP-8	Industrial	XP16V with Pb-Free bar-line indicator	NiPdAu Pb-Free

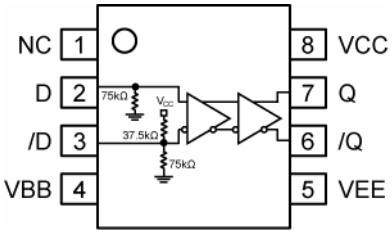
Notes:

1. Contact factory for die availability. Dice are guaranteed at T_A = 25°C, DC Electricals only.
2. Tape and Reel.

Pin Configuration



8-Pin MLF® (MLF-8)



8-pin SOIC (SOIC-8)
8-pin MSOP (MSOP-8)

Pin Description

Pin Number	Pin Name	Pin Function
2, 3	D, /D	ECL Inputs. The D input has an internal 75kΩ pull-down resistor and the /D has an internal 37.5kΩ pull-up and a 75kΩ pull-down resistor.
7, 6	Q, /Q	ECL Outputs. Terminate with 50Ω to V_{CC} -2V. See “Output Interface Applications” section for more details.
4	V_{BB}	Reference Output Voltage: This reference is typically used to bias the unused inverting input for single-ended input applications, or as the termination point for AC-coupled differential input applications. V_{BB} reference value is approximately V_{CC}- 1.42V, and tracks V_{CC} 1:1. Maximum sink/source capability for V_{BB} is 0.50mA. For single ended inputs, connect to the unused input through a 50Ω resistor. Decouple the V_{BB} pin with a 0.01µF capacitor to V_{CC}.
8	V_{CC}	Positive Power Supply: Bypass with 0.1µF//0.01µF low ESR capacitors as close to the V_{CC} pin as possible.
5	V_{EE}	Negative Power Supply: V_{EE} and exposed pad must be tied to the most negative supply. Exposed pad is available for 8-pin MLF® package only.

Absolute Maximum Ratings[1]

Supply Voltage
- PECL Mode (V_{CC}) .....+6V
- NECL Mode (V_{EE}) .....–6V

Current (I_{BB})
- Source or Sink current on V_{BB} .....±0.5mA

Input Voltage (V_{IN})
- PECL Mode ..... V_{CC} to 0V
- NECL Mode ..... 0V to V_{EE}

Output Current (I_{OUT})
- Continuous.....50mA
- Surge 100mA

Lead Temperature (soldering, 20sec.).....260°C

Storage Temperature (T_s) .....–65°C to +150°C

Operating Ratings[2]

Supply Voltage (V_{CC})
- PECL Mode (V_{EE} = 0V) .....+3.0V to +5.5V

Supply Voltage (V_{EE})
- NECL Mode (V_{CC} = 0V).....-3.0V to -5.5V

Ambient Temperature (T_A).....–40°C to +125°C

Junction Thermal Resistance
- MLF® (θ_{JA}) ..... 90°C/W
- MSOP (θ_{JA})..... 160°C/W
- SOIC (θ_{JA}) ..... 99°C/W

100EP DC Electrical Characteristics[3]

V_{CC} = 3.3V ±10%, V_{EE} = 0V; T_A = –40°C to +85°C, unless otherwise stated[4]

Symbol	Parameter	-40°C			25°C			85°C			Units
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
I_{EE}	Power Supply Current		22	30		22	30		22	30	mA
V_{OH}	Output HIGH Voltage[5]	2155	2280	2405	2155	2280	2405	2155	2280	2405	mV
V_{OL}	Output LOW Voltage[5]	1355	1480	1605	1355	1480	1605	1355	1480	1605	mV
V_{IH}	Input HIGH Voltage	2075		2420	2075		2420	2075		2420	mV
V_{IL}	Input LOW Voltage	1355		1675	1355		1675	1355		1675	mV
V_{BB}	Output Voltage Reference	1775	1875	1975	1775	1875	1975	1775	1875	1975	mV
V_{IHCMR}	Input HIGH Voltage Common Mode Range (Differential Configuration)[6]	2.0		3.3	2.0		3.3	2.0		3.3	V
I_{IH}	Input HIGH Current			150			150			150	µA
I_{IL}	Input LOW Current D /D	0.5 -150			0.5 -150			0.5 -150			µA

Notes:

1. Permanent device damage may occur if absolute maximum ratings are exceeded. This is a stress rating only and functional operation is not implied at conditions other than those detailed in the operational sections of this data sheet. Exposure to absolute maximum ratings conditions for extended periods may affect device reliability.
2. The datasheet limits are not guaranteed if the device is operated beyond the operating ratings.
3. The circuit is designed to meet the DC specifications shown in the above table after thermal equilibrium has been established.
4. Input and output parameters vary 1:1 with V_{CC}.
5. All loading with 50Ω to V_{CC}–2.0V.
6. V_{IHCMR} (min) varies 1:1 with V_{EE}, V_{IHCMR} (max) varies 1:1 with V_{CC}. The V_{IHCMR} range is referenced to the most positive side of the differential input signal\

100EP DC Electrical Characteristics[7]

V_{CC} = 5V ±10%, V_{EE} = 0V; T_A = –40°C to +85°C, unless otherwise stated.[8]

Symbol	Parameter	-40°C			25°C			85°C			Units
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
I_{EE}	Power Supply Current		22	30		22	30		22	30	mA
V_{OH}	Output HIGH Voltage[9]	3855	3980	4105	3855	3980	4105	3855	3980	4105	mV
V_{OL}	Output LOW Voltage[9]	3055	3180	3305	3055	3180	3305	3055	3180	3305	mV
V_{IH}	Input HIGH Voltage (Single-Ended)	3775		4120	3775		4120	3775		4120	mV
V_{IL}	Input LOW Voltage (Single-Ended)	3055		3375	3055		3375	3055		3375	mV
V_{BB}	Output Voltage Reference	3475	3575	3675	3475	3575	3675	3475	3575	3675	mV
V_{IHCMR}	Input HIGH Voltage Common Mode Range (Differential Configuration)[10]	2.0		5.0	2.0		5.0	2.0		5.0	V
I_{IH}	Input HIGH Current			150			150			150	µA
I_{IL}	Input LOW Current D /D	0.5 -150			0.5 -150			0.5 -150			µA

100EP DC Electrical Characteristics[7]

V_{CC} = 0V, V_{EE} = -5.5V to –3.0V; T_A = –40°C to +85°C, unless otherwise stated.[8]

Symbol	Parameter	-40°C			25°C			85°C			Units
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
I_{EE}	Power Supply Current		22	30		22	30		22	30	mA
V_{OH}	Output HIGH Voltage[9]	- 1145	- 1020	- 0895	- 1145	- 1020	- 0895	- 1145	- 1020	- 895	mV
V_{OL}	Output LOW Voltage[9]	- 1945	-1820	-1695	- 1945	-1820	-1695	- 1945	-1820	-1695	mV
V_{IH}	Input HIGH Voltage (Single-Ended)	-1225		-0.880	-1225		-0.880	-1225		-0.880	mV
V_{IL}	Input LOW Voltage (Single-Ended)	- 1945		-1625	- 1945		-1625	- 1945		-1625	mV
V_{BB}	Output Voltage Reference	-1525	-1425	- 1325	-1525	-1425	- 1325	-1525	-1425	- 1325	mV
V_{IHCMR}	Input HIGH Voltage Common Mode Range (Differential Configuration)[10]	V_{EE} +2.0		0	V_{EE} +2.0		0	V_{EE} +2.0		0	V
I_{IH}	Input HIGH Current			150			150			150	µA
I_{IL}	Input LOW Current D /D	0.5 -150			50 -150			50 -150			µA

Notes:

7. The circuit is designed to meet the DC specifications shown in the above table after thermal equilibrium has been established.
8. Input and output parameters vary 1:1 with V_{CC}.
9. All loading with 50Ω to V_{CC}–2.0V.
10. V_{IHCMR} (min) varies 1:1 with V_{EE}, V_{IHCMR} (max) varies 1:1 with V_{CC}. The V_{IHCMR} range is referenced to the most positive side of the differential input signal.

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AC Electrical Characteristics

V_{CC} = 0V, V_{EE} = -5.5V to –3.0V; V_{CC} = 3.0V to 5.5V, V_{EE} = 0V; T_A = –40°C to +85°C, unless otherwise stated.[11]

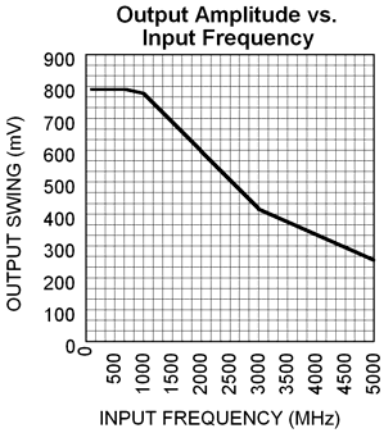
Symbol	Parameters	-40°C			25°C			85°C			Units
		Min	Typ	Max	Min	Typ	Max	Min	Typ	Max	
f_{MAX}	Maximum Frequency		2.5			2.5			2.5		Gbps
		3	4		3	4		3	4		GHz
t_{PLH} t_{PHL}	Propagation Delay to Output Differential D→Q, /Q (Figure 1)	150	220	280	150	220	280	160	240	300	ps
t_{Skew}	Duty Cycle Skew[12]		5	20		5	20		5	20	ps
t_{JITTER}	Cycle-to-Cycle Jitter[13]		0.2	1		0.2	1		0.2	1	ps
V_{PP}	Input Voltage Swing (Differential Configuration)	150	800	1200	150	800	1200	150	800	1200	mV
t_r, t_f	Output Rise/Fall Times (20% to 80%) Q, /Q	70	120	170	80	130	180	100	150	200	ps

Notes:

11. Measured using a 750mV source, 50% duty cycle input. 50Ω to V_{CC} -2V loading.
12. Skew is measured between outputs under identical transitions. It is defined only for differential operation where the delays are measured from the cross point of the inputs to the cross point of the outputs.
13. Cycle-to-cycle jitter definition: the variation period between adjacent cycles over a random sample of adjacent cycle pairs. $t_{JITTER_CC} = T_{n} - T_{n+1}$, where T is the time between rising edges of the output signal.

Typical Characteristics

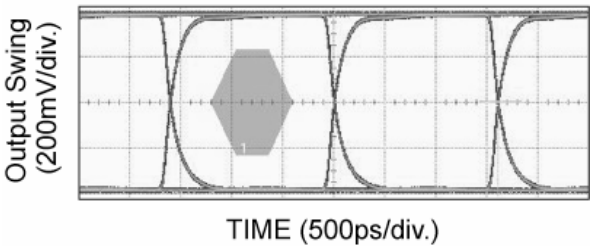
V_{CC} = 3.3V, GND = 0V, V_{IN} = 800mV, R_L = 50Ω to V_{CC}-2V, T_A = 25°C, unless otherwise stated.



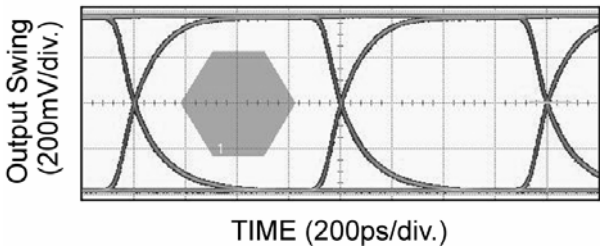
Functional Characteristics

V_{CC} = 3.3V, GND = 0V, V_{IN} = 150mV, Data Pattern: 2^{23}-1, R_L = 50Ω to V_{CC}-2V, T_A = 25°C, unless otherwise stated.

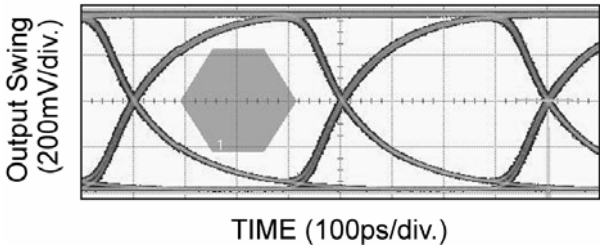
622Mbps Output



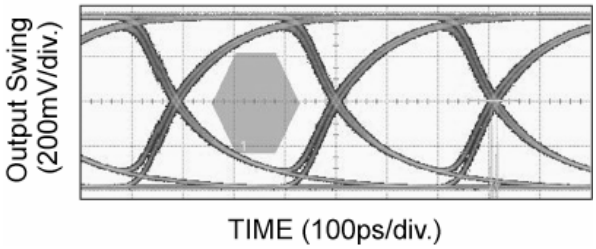
1.25Gbps Output



2.5Gbps Output

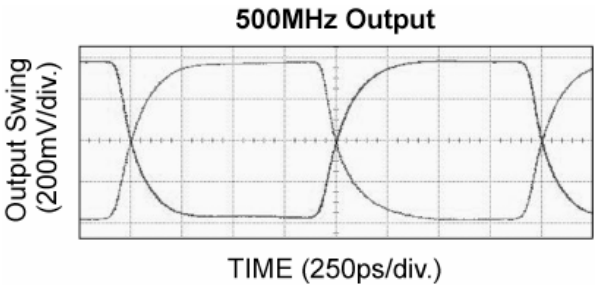


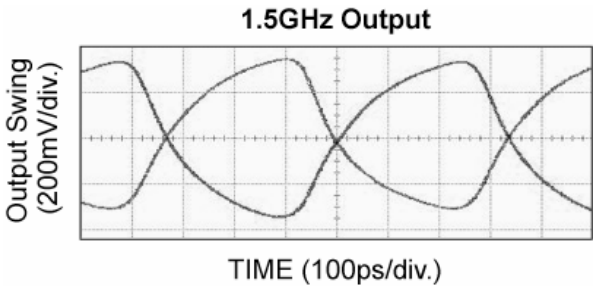
3.2Gbps Output

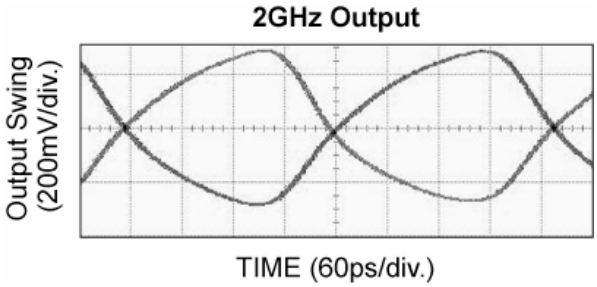


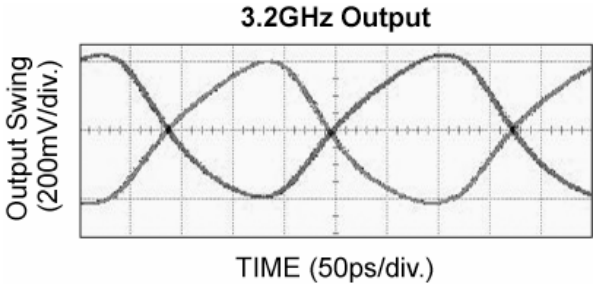
Functional Characteristics (continued)

V_{CC} = 3.3V, GND = 0V, V_{IN} = 150mV, R_L = 50Ω to V_{CC}-2V, T_A = 25°C, unless otherwise stated.









Single-Ended and Differential Swings



Figure 1a. Single-Ended Swing

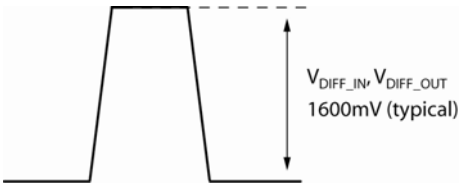


Figure 1b. Differential Swing

Output Interface Applications

ECL outputs have very low output impedance (open emitter), and small signal swing which results in low EMI. ECL is ideal for driving 50Ω-and-100Ω-controlled impedance transmission lines. There are several techniques in terminating the ECL output, as shown in Figure 2a and 2b.

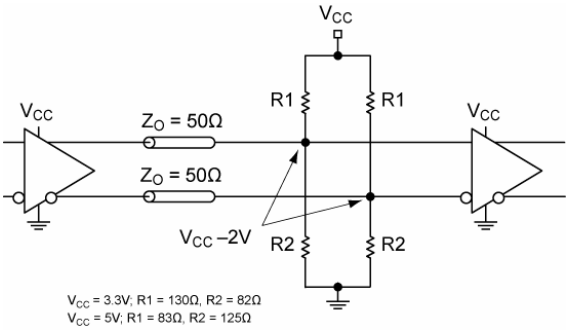


Figure 2a. Parallel Termination-Thevenin Equivalent

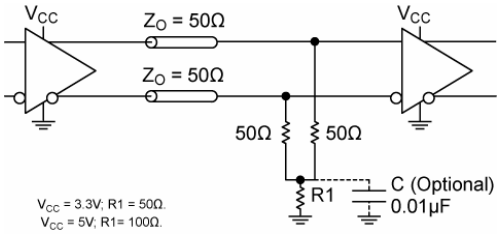
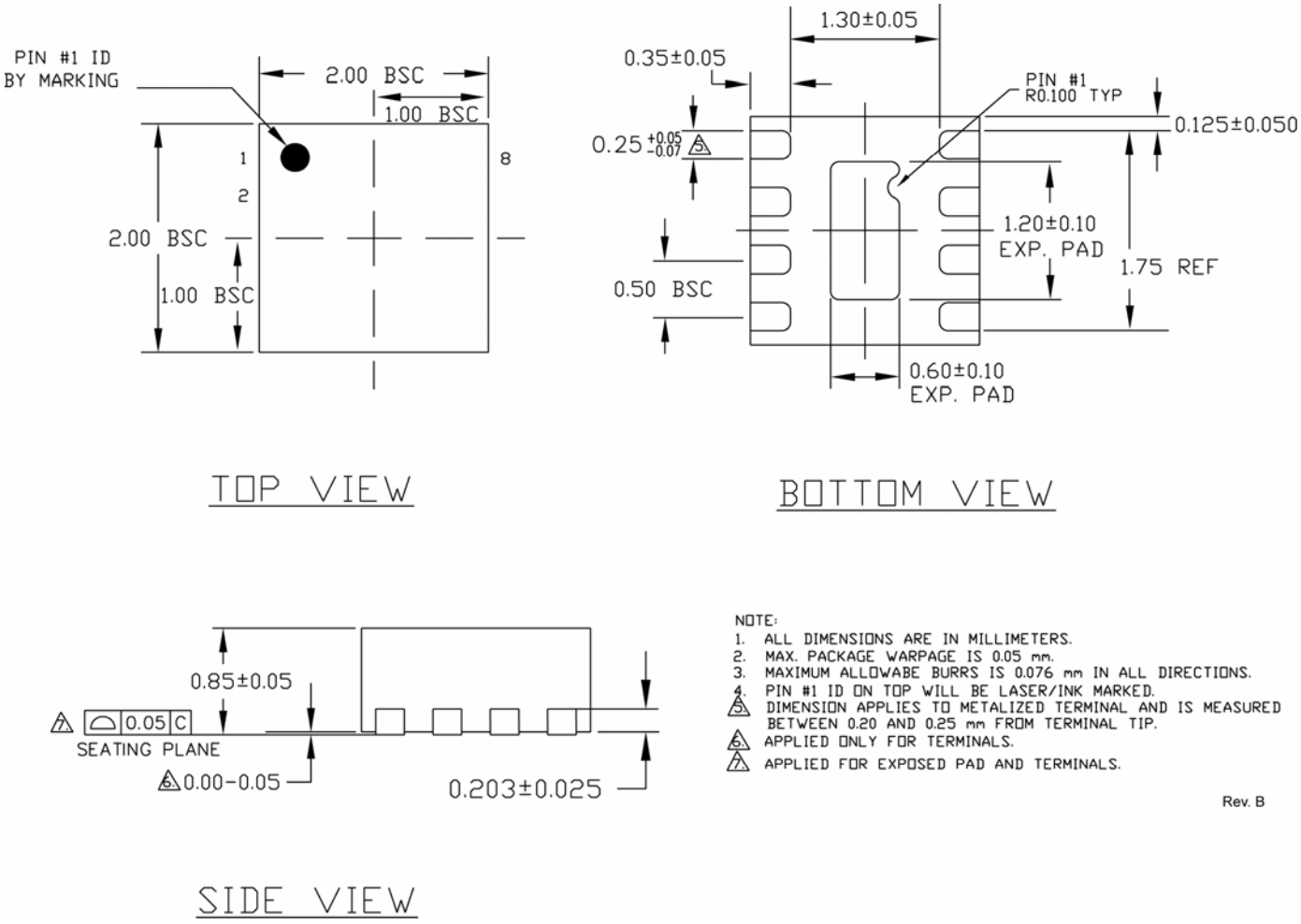


Figure 2b. Three-Resistor "Y-Termination"

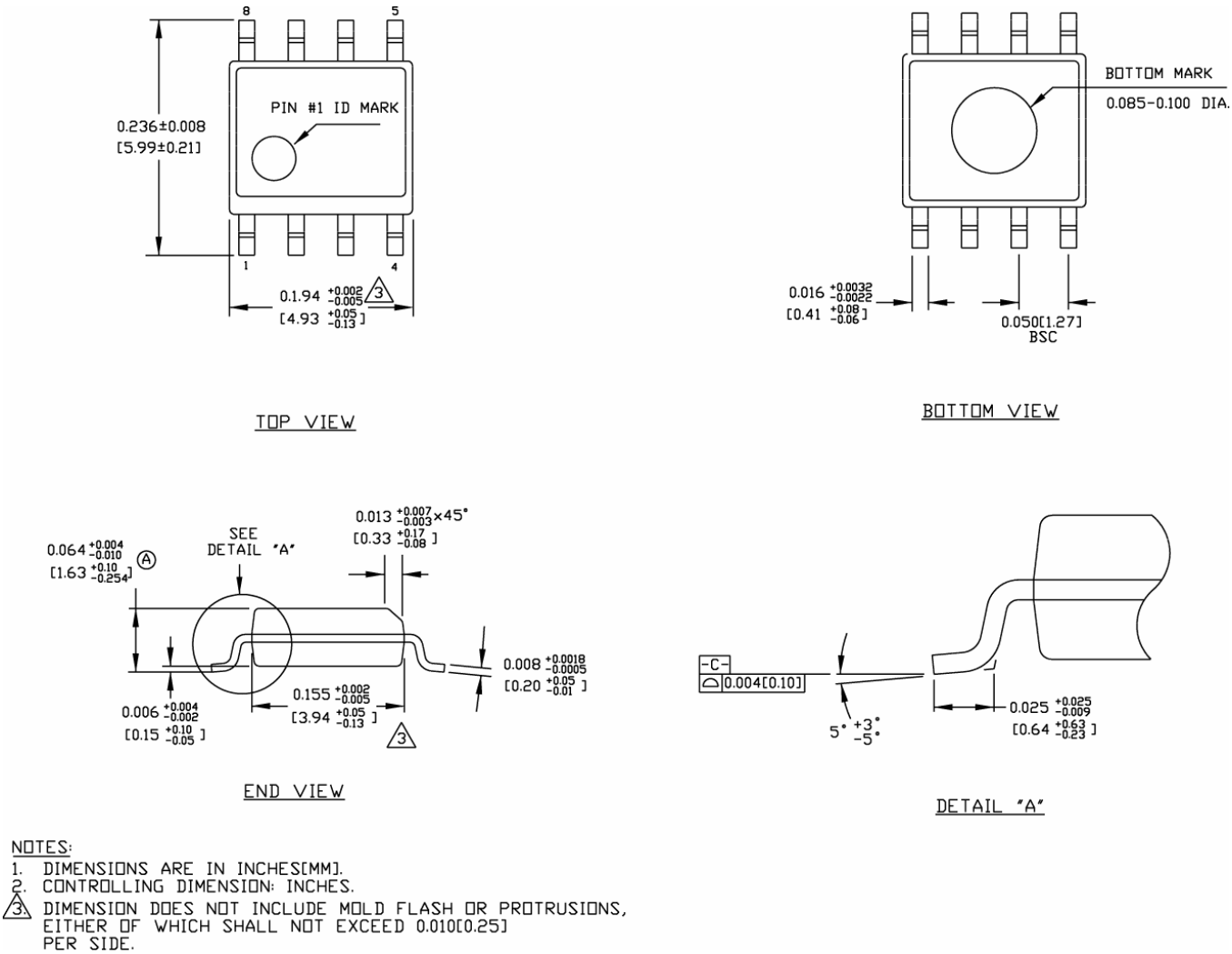
Related Product and Support Documents

Part Number	Function	Datasheet Link
HBW Solutions	New Products and Termination Application Notes	http://www.micrel.com/page.do?page=/product-info/as/HBWsolutions.shtml

Package Information

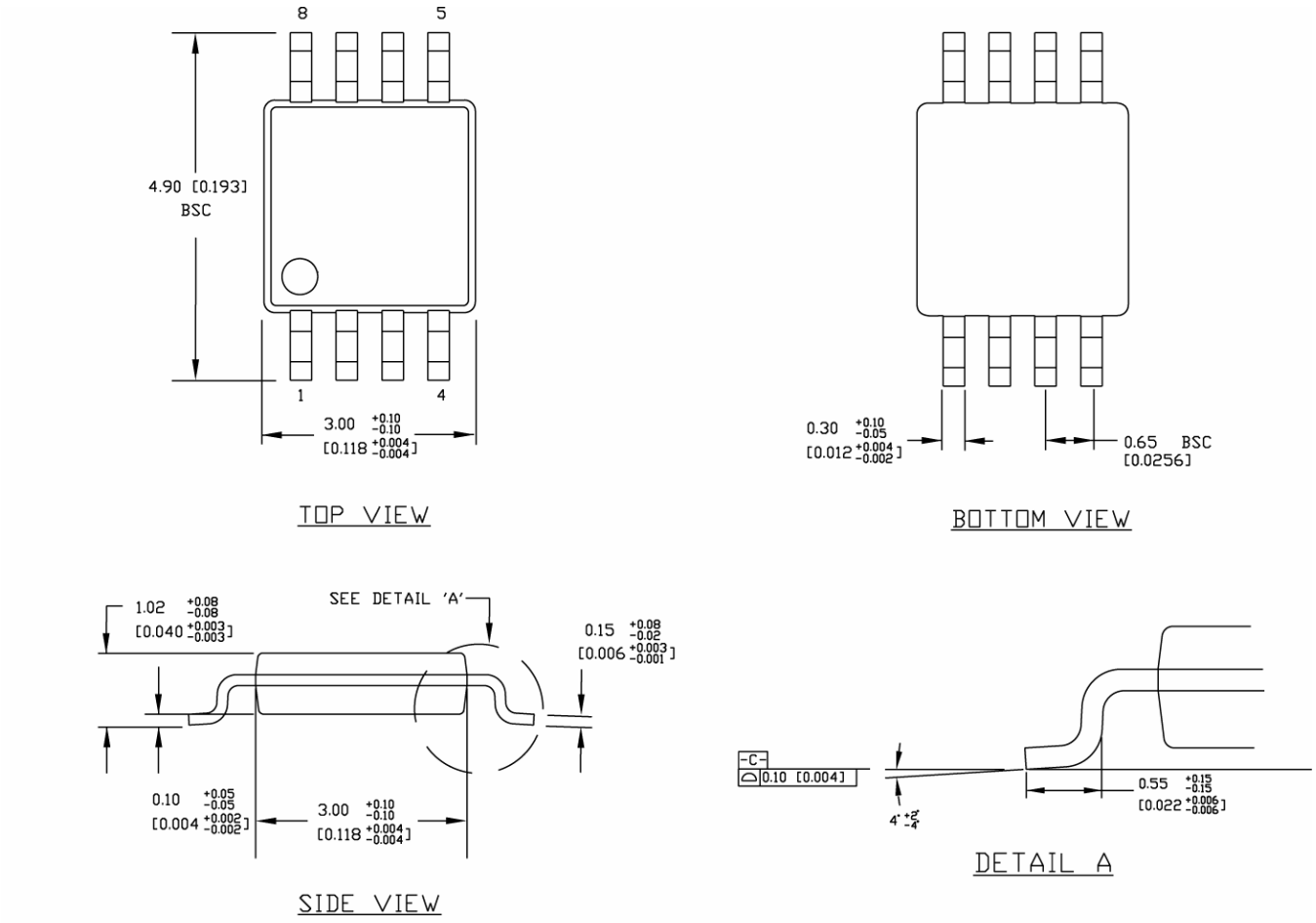


8-Pin (2mm x 2mm) MLF® (MLF-8)



NOTES:
1. DIMENSIONS ARE IN INCHES[MM].
2. CONTROLLING DIMENSION: INCHES.
3. DIMENSION DOES NOT INCLUDE MOLD FLASH OR PROTRUSIONS, EITHER OF WHICH SHALL NOT EXCEED 0.010[0.25] PER SIDE.

8-Pin SOIC (SOIC-8)



NOTES:
1. DIMENSIONS ARE IN MM [INCHES].
2. CONTROLLING DIMENSION: MM
3. DIMENSION DOES NOT INCLUDE MOLD FLASH OR PROTRUSIONS, EITHER OF WHICH SHALL NOT EXCEED 0.20 [0.008] PER SIDE.

8-Pin MSOP (MSOP-8)

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