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NCP692MN33T2GEVB: 3.3 V / 1 A CMOS LDO Evaluation Board

The NCP692 CMOS LDO family provides 1 A of output current with enhanced ESD in fixed output voltage options from 1.5 V to 5.0 V. These devices are designed for space constrained and portable battery powered applications and offer additional features such as low Dropout Voltage, high Power Supply Rejection Ratio (PSRR), low Quiescent and Ground Current consumption, low Noise operation, Short Circuit and Thermal Protection. NCP692 is designed to be used with low cost ceramic capacitors and the minimum value of 1µF output capacitance is required. The NCP692 device is equipped with Active High Enable pin, Active Output Discharge, Current Limit and Thermal Shutdown Protection. Finally the Surface Mount DFN3x3 package with Expose Pad allows saving PCB space and effectively dissipating heat through the PCB copper area. This demonstration board operates from a dc input voltage VIN < 6V and produces fixed output voltage given by the NCP692 internal voltage divider. External waveform generator could be connected to the EN (Enable) pin in order to verify the ON/OFF operation.



Evaluation/Development Tool Information

Product	Status	Compliance	Short Description	Parts Used	Action
NCP692MN33T2GEVB	Active	Pb-free	3.3 V / 1 A CMOS LDO Evaluation Board	NCP692MN33T2G	Contact Local Sales Office Inventory

Technical Documents

Type	Document Title	Document ID/Size	Rev
Eval Board: BOM	NCP692MN33T2GEVB Bill of Materials ROHS Compliant	NCP692MN33T2GEVB_BOM_ROHS.PDF - 98.0 KB	0
Eval Board: Gerber	NCP692MN33T2GEVB Gerber Layout Files (Zip Format)	NCP692MN33T2GEVB_GERBER.ZIP - 19.0 KB	0
Eval Board: Schematic	NCP692MN33T2GEVB Schematic	NCP692MN33T2GEVB_SCHEMATIC.PDF - 129.0 KB	0
Eval Board: Test Procedure	NCP692MN33T2GEVB Test Procedure	NCP692MN33T2GEVB_TEST_PROCEDURE.PDF - 313.0 KB	0

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