SiliconMotion

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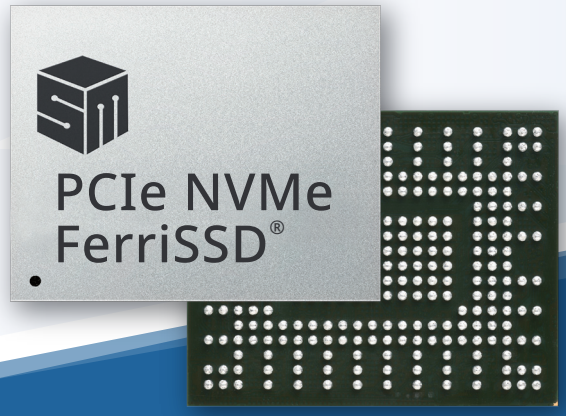
PCIe NVMe FerriSSD®

Single-Chip SSD



SM681 Series Single-Chip SSD

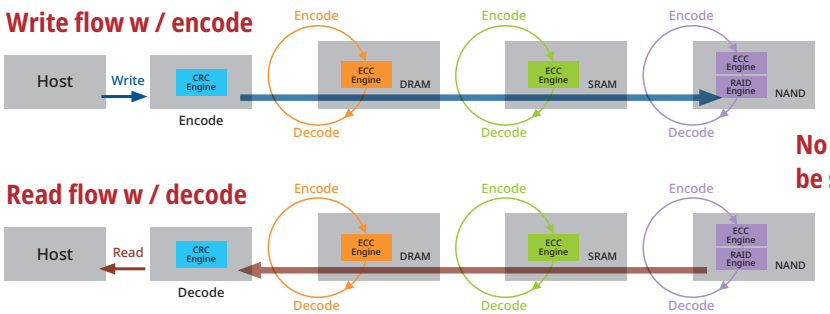
The SM681 single-chip SSD supports PCIe Gen3 x2 NVMe 1.3 designed optimally for mission critical applications. By combining industry proven controller technology, NAND flash and passive components into a small single BGA package, PCIe NVMe FerriSSD® simplifies design efforts, reduces time-to-market while protecting from NAND technology migration concerns.

The SM681 DRAM-Less series feature the best balance of saving/performance - cost saving from eliminating DRAM while maintaining DRAM-like performance via HMB (Host Memory Buffer). Both available in 3D TLC/SLC modes, the unique flexible design can support multiple capacity configurations ranging from 8GB to 480GB and include enterprise-grade advanced data integrity and reliability capabilities using Silicon Motion's proprietary end-to-end data protection, ECC and data caching technologies.

Key Features

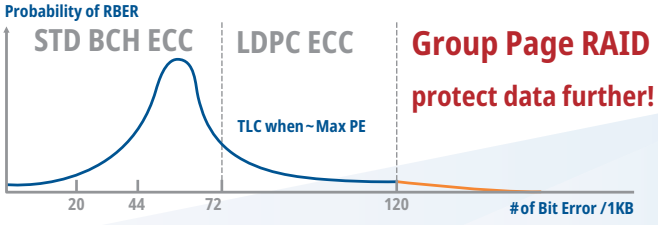
End to End Data Path Protection

SMI's PCIe NVMe FerriSSDs incorporate full data error detection with recovery engines to provide enhanced data integrity throughout the entire Host-to-NAND-to-Host data path. The PCIe NVMe FerriSSD® data recovery algorithm can effectively detect any error in the SSD data path, including hardware (i.e. ASIC) errors, firmware errors and memory errors arising in SRAM, DRAM or NAND.



NANDXtend™ ECC Engine

Conventional SSDs employ standard BCH and RS ECC (error correction coding) engines for initiate first-level correction using NAND shift-read-retries. In addition to this first-level error correction, PCIe NVMe FerriSSDs also implement a highly efficient second-level correction scheme using an LDPC (low-density parity check) code and a Group page RAID algorithm (a highly efficient redundant backup) to reduce potential dPPM at customer site while extending the service life of SSD.



Key Features

IntelligentScan and DataRefresh to Enhance Data Integrity

SMI's proprietary IntelligentScan function will activate automatically to scan recharge, repair or retire the cell block (DataRefresh) according to the host behavior and working environment (eg. ambient temperature). As a result of the combination of IntelligentScan and DataRefresh, PCIe NVMe FerriSSD® can effective prolong its service life much beyond typical NAND specifications.

Thermo impact on NAND Data Retention

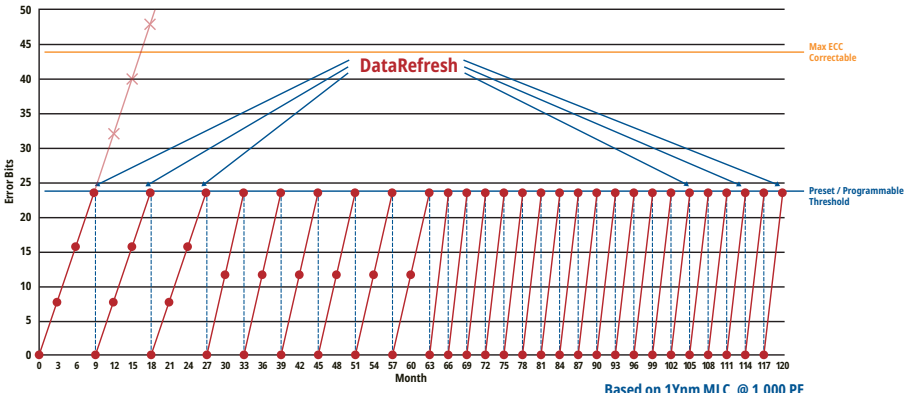
Temp	SLC @ max PE	MLC @ max PE
40	75.58 Mo	12 Mo
55	12 Mo	1.88 Mo
70	2.14 Mo	0.34 Mo
85	0.45 Mo	0.07 Mo

Based on Arrhenius Equation



Higher ambient temp to increase Scan frequency

85℃ data retention simulation



Based on 1Ynm MLC, @ 1,000 PE

IntelligentScan/DataRefresh to proactively extend Data Retention beyond the typical NAND flash limitation

Not to scale, for illustration purpose

Why PCIe NVMe FerriSSD®

Easy to use

- Plug & Play only requires format/fdisk prior to use
- Small footprint for space-limited design

Lower total cost of ownership

- Rugged & Reliable (no moving parts)
- Eliminate requalification cost from NAND generation change
- Cost saving with flexible TLC/MLC/SLC modes, configurable capacities.

Eliminate down time

- Support S.M.A.R.T. and advanced SSD Telemetry logging features
- IntelligentScan with DataRefresh for Data integrity enhancement
- Full End-to-End data path protection with recovery algorithms
- SMI's 4th generation LDPC ECC engine with Group Page RAID
- Remote firmware update available via secured digital signature

Specifications

	SM681
Host Interface	PCIe Gen3 x2
PCIe Protocal	NVMe 1.3
Embedded DRAM	DRAM-less
Form Factor	20mm x 16mm BGA
Green Product	Compliant to RoHS (Restriction to Hazardous Substances Directive) 2.0 / Halogen free
Temperature Support	Commercial Temp (0°C to + 70°C) Industrial Temp (-40°C to + 85°C)

Density

3D SLCmode	*8~160GB**
3D TLCmode	*32~480GB***

**320GB SLCmode in Q1'2024*
***1TB TLCmode in Q4'2023*

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